

Amendments to the Claims:

Please amend Claims 1, 2, 6, 17, 19, 21 and 22 as set forth below. This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A data preservation system for flash memory systems connected with a host system, the flash memory system receiving a host system power supply and energizing a supplemental energy store therewith and communicating with the host system via an interface bus, wherein, upon loss of the host system power supply, the flash memory system actively isolates the connection to the host system power supply and isolates the interface bus and employs the supplemental energy store to complete pending write operations to flash memory.

2. (Currently Amended) A data preservation system for flash memory systems receiving a power supply and experiencing power failure thereof, the data preservation system comprising:
a detection circuit in communication with the power supply;
an auxiliary power source;
an isolation circuit for isolating the auxiliary power source upon detection of a power failure by the detection circuit; and
controller circuitry configured to complete pending operations storing ~~store~~ data ~~in~~ from volatile memory into flash memory using the auxiliary power source.

3. (Original) The data preservation system of Claim 2, wherein the volatile memory comprises a tri-state buffer.

4. (Original) The data preservation system of Claim 2, wherein the detection circuit comprises a voltage detector.

5. (Original) The data preservation system of Claim 2, wherein the auxiliary power source comprises capacitors.

6. (Currently Amended) A method of preserving data in flash memory systems experiencing a power failure, the method comprising:

- charging an auxiliary power source with a supply voltage;
- detecting a loss of power of the supply voltage;
- isolating the auxiliary power source; and
- utilizing the auxiliary power source to complete pending operations storing store data stored in volatile memory into flash memory.

7. (Original) The method of Claim 6, wherein isolating the auxiliary power source comprises opening a relay interconnecting the supply voltage and the auxiliary power source.

8. (Original) The method of Claim 6, further comprising isolating a host system data bus from the flash memory system.

9 to 16. (Cancelled)

17. (Currently Amended) A data preservation system comprising:

- a power detector;
- an auxiliary power source;
- an isolator adapted to isolate the auxiliary power source when the power detector detects a loss of power; and
- a data store configured to complete pending operations storing data into non-volatile memory powered by the auxiliary power source when the power detector detects a loss of power.

18. (Previously presented) The system of Claim 17, wherein the non-volatile memory comprises flash memory.

19. (Currently amended) A method for storing data in a memory device, the method comprising:

- detecting a power reduction;
- decoupling an auxiliary power source upon detecting a power reduction; and
- completing pending operations storing data into non-volatile memory using the auxiliary power source.

20. (Original) The method of Claim 19, further comprising decoupling a volatile memory from external connections.

21. (Currently amended) The method of Claim 20, ~~further comprising~~ wherein the pending operations are storing data from the volatile memory into the non-volatile memory.

22. (Currently amended) ~~Means for preserving data in a~~ A memory device comprising:

- means for detecting loss of power;
- means for providing auxiliary power;
- means for isolating the ~~means for preserving data~~ memory device upon detection of loss of power; and
- means for completing pending operations storing data in a non-volatile manner utilizing the auxiliary power.